

# CMP40N25P/CMB40N25P

250V N-Channel MOSFET

## General Description

The 40N25P uses advanced planar stripe DMOS technology and design to provide excellent RDS(ON).

These devices are well suited for high efficiency switched mode power supplies, active power factor correction based on half bridge topology.

## Features

- Fast switching
- 100% avalanche tested
- Improve dv/dt capability
- RoHS compliant

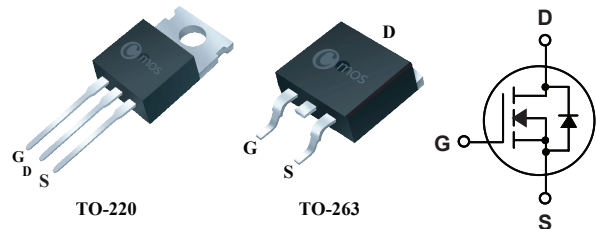
## Product Summary

BVDSS	R <sub>DS(on)</sub> max.	ID
250V	90mΩ	40A

## Applications

- Uninterruptible power supplies
- DC/DC converter
- DC/AC inverter

## TO-220/263 Pin Configuration



Type	Package	Marking
CMP40N25P	TO-220	CMP40N25P
CMB40N25P	TO-263	CMB40N25P

## Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V <sub>DS</sub>	Drain-Source Voltage	250	V
V <sub>GS</sub>	Gate-Source Voltage	±30	V
I <sub>D</sub> @T <sub>C</sub> =25°C	Continuous Drain Current	40	A
I <sub>D</sub> @T <sub>C</sub> =100°C	Continuous Drain Current	32	A
I <sub>DM</sub>	Pulsed Drain Current <sup>1</sup>	160	A
EAS	Single Pulse Avalanche Energy <sup>2</sup>	1000	mJ
P <sub>D</sub> @T <sub>C</sub> =25°C	Total Power Dissipation	160	W
T <sub>STG</sub>	Storage Temperature Range	-55 to 175	°C
T <sub>J</sub>	Operating Junction Temperature Range	-55 to 175	°C

## Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
R <sub>θJA</sub>	Thermal Resistance Junction-ambient	---	62.5	°C/W
R <sub>θJC</sub>	Thermal Resistance Junction-case	---	0.78	°C/W

**Electrical Characteristics ( $T_J=25^\circ\text{C}$  , unless otherwise noted)**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	250	---	---	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance	$V_{GS}=10V, I_D=20A$	---	---	90	m $\Omega$
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}, I_D=250\mu A$	2	---	4	V
$I_{DSS}$	Drain-Source Leakage Current	$V_{DS}=200V, V_{GS}=0V$	---	---	1	$\mu A$
$I_{GSS}$	Gate-Source Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	---	---	$\pm 100$	nA
gfs	Forward Transconductance	$V_{DS}=10V, I_D=25A$	---	23	---	S
$Q_g$	Total Gate Charge	$I_D=20A$	---	63	---	nC
$Q_{gs}$	Gate-Source Charge	$V_{DS}=125V$	---	17	---	
$Q_{gd}$	Gate-Drain Charge	$V_{GS}=10V$	---	19	---	
$T_{d(on)}$	Turn-On Delay Time	$V_{DS}=125V$ $I_D=20A$ $R_G=25\Omega$	---	43	---	ns
$T_r$	Rise Time		---	27	---	
$T_{d(off)}$	Turn-Off Delay Time		---	156	---	
$T_f$	Fall Time		---	33	---	
$C_{iss}$	Input Capacitance	$V_{DS}=25V, V_{GS}=0V, f=1MHz$	---	2700	---	pF
$C_{oss}$	Output Capacitance		---	325	---	
$C_{rss}$	Reverse Transfer Capacitance		---	40	---	

**Diode Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_S$	Continuous Source Current	$V_G=V_D=0V$ , Force Current	---	---	40	A
$I_{SM}$	Pulsed Source Current		---	---	160	A
$V_{SD}$	Diode Forward Voltage	$V_{GS}=0V, I_F=40A, T_J=25^\circ\text{C}$	---	---	1.4	V
$t_{rr}$	Reverse Recovery Time	$V_{GS}=0V, I_F=20A$ $di_F/dt=100A/\mu s$	---	167	---	ns
$Q_{rr}$	Reverse Recovery Charge		---	1.25	---	$\mu C$

Note :

- 1.Repetitive rating; pulse width limited by maximum junction temperature.
- 2.The test condition is  $V_{DD}=50V, V_{GS}=10V, L=1mH, I_{AS}=40A$ .

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